IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Eliyahou Harari et al.

Title:

Flash EEprom System

Application No.:

09/867,836

Filing Date:

May 30, 2001

Examiner:

Tran, Andrew Q.

Group Art Unit:

2824

Docket No.:

SNDK.A06US7

Conf. No.:

6307

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Patent no. 5,172,338 and parent patent no. 5,991,517 are being asserted by their assignee SanDisk Corporation in a matter before the United States International Trade Commission, Investigation no. 337-TA-560. This Statement is being filed to bring information arising from this matter to the attention of the Examiner.

An "Expert Report of Dr. Richard Pashley . ." includes opinions that certain claims of the '338 and '517 patents are invalid over the prior art and unenforceable due to inequitable conduct in obtaining their grant. Also included in this Statement are references discussed in this expert report that have not already been submitted in the present application. A "Rebuttal Expert Report of G.R. Mohan Rao, Ph.D. . ." submitted on SanDisk's behalf in that matter rebuts the invalidity opinions of Dr. Pashley's report.

This ITC matter is still pending, an evidentiary hearing being scheduled for December, 2006.

This information disclosure statement is submitted under 37 C.F.R. § 1.97(c). The fee of \$180.00 has been authorized via EFS to Deposit Account 502664. The Commissioner is hereby

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authorized to charge any additional fees, which may be required, or credit any overpayment to Deposit Account 502664.

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Respectfully submitted,

Soudd P. PAZZ

November 22, 2006

Date

Gerald P. Parsons

Reg. No. 24,486

PARSONS HSUE & DE RUNTZ LLP

595 Market Street, Suite 1900

San Francisco, CA 94105

(415) 318-1160 (main)

(415) 318-1163 (direct)

(415) 693-0194 (fax)

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Application No.: 09/867,836

U.S. Depa	rtment	of Commerce, Patent a	and Trademark	Atty. Docket No.	Atty. Docket No.			Application No.	
INFORM	ATION	DISCLOSURE STA	TEMENT BY	SNDK.A06US7	SNDK.A06US7		09/867,836		
		APPLICANT		Applicants	Applicants		Conf. No.		
(Use several sheets if necessary)				Eliyahou Harari et al.		6307			
(Form PTO-1449)				Filing Date		Art Group			
				May 30, 2001		2824			
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	5.	S. Mehrotra et al., "Serial 9Mb Flash EEPROM for Solid State Disk Applications," 1992 Symposium on VLSI Circuits Digest of Technical Papers, IEEE, pp. 24-25. David A. Rich, "A Survey of Multivalued Memories," IEEE Transaction of Computers, Vol. C-35, No. 2,							
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	8.	Electronic Industries Association, JEDEC Stnadard, "Configurations for Solid State Memories, JEDEC Standard No. 21-B (Revision of JESD21-A and JESD21-A-1), CS-0336, 337-TA-560, December, 1988, pp. i-36, 39-147.							
	9	"Expert Report of Dr. Richard Pashley Regarding the Invalidity of U.S. Patent No. 5,172,338 and U.S. Patent No. 5,991,517," United States International Trade Commission, Investigation No. 337-TA-560, September 7, 2006, pp. i-387.							
	10	Nos. 5,172,338 and	5,991,517," Unit	nan Rao, Ph.D. Regardi ed States International Exhibit List, 6 pages.					
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